

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Yeo, *et al.* Docket No.: TSM03-0847
Serial No.: 10/826,602 Art Unit: 1795
Filed: April 16, 2004 Examiner: Raymond, Brittany L.
For: Lithography Apparatus for Manufacture of Integrated Circuits

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed forms PTO/SB/08a & b that may be considered material to the examination of the above-identified application. Copies of the U.S. Patent and Publications cited are not being submitted. However, Applicants have included copies of the non-patent literature.

This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(c) together with a \$180.00 fee under 37 C.F.R. §1.17(p) after the C.F.R. §1.97(b) time period, but before final action or notice of allowance, whichever occurs first. Please charge the required fee of \$180.00 and any additional amount, or credit any overpayment to Deposit Acct. No 50-1065 of the below mentioned firm.

Respectfully submitted,

November 24, 2008
Date

Slater & Matsil, L.L.P.
17950 Preston Rd., Suite 1000
Dallas, TX 75252
(972) 732-1001 (phone)
(972) 732-9218 (fax)

/Roger C. Knapp/
Roger C. Knapp
Attorney for Applicants
Reg. No. 46,836